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FIRST NAMED INVENTOR APPLICATION NO. FILING DATE ATTORNEY DOCKET NO. 09/182,911 10/30/98 WILKS В 0100.9800830 **EXAMINER** LM01/0405 MARKISON & RECKAMP LESPERANCE, J 175 WEST JACKSON BOULEVARD ART UNIT PAPER NUMBER SUITE 105 CHICAGO IL 60604 2774 **DATE MAILED:** 04/05/00 •

Please find below and/or attached an Office communication concerning this application or proceeding.

**Commissioner of Patents and Trademarks** 

## Office Action Summary

Application No. 09/182,911

Jean Lesperance

Applicant(=)

Examiner

Group Art Unit

Barry G. Wilks

2774



Responsive to communication(s) filed on	
☐ This action is <b>FINAL</b> .	
☐ Since this application is in condition for allowance except for formal matters, in accordance with the practice under Ex parte Quay@35 C.D. 11; 453 O.G. 213.	as to the merits is closed
A shortened statutory period for response to this action is set to expire3month(s), o longer, from the mailing date of this communication. Failure to respond within the period for respapplication to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under 37 CFR 1.136(a).	oonse will cause the
Disposition of Claim	
	_ is/are pending in the applicat
Of the above, claim(s) is/a	re withdrawn from consideration
Claim(s)	is/are allowed.
☐ Claim(s)	
☐ Claims are subject to re	
Application Papers	
∑ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.	
☐ The drawing(s) filed on is/are objected to by the Examiner.	
☐ The proposed drawing correction, filed on is ☐ approved ☐ dis	sapproved.
☐ The specification is objected to by the Examiner.	
☐ The oath or declaration is objected to by the Examiner.	
Priority under 35 U.S.C. § 119	
☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).	
☐ All ☐Some* None of the CERTIFIED copies of the priority documents have beer	١
received.	
☐ received in Application No. (Series Code/Serial Number)	•
☐ received in this national stage application from the International Bureau (PCT Rule 1	17.2(a)).
*Certified copies not received:	
Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).	
Attachment(s)	
Notice of References Cited, PTO-892	
☐ Information Disclosure Statement(s), PTO-1449, Paper No(s) ☐ Interview Summary, PTO-413	
☑ Notice of Draftsperson's Patent Drawing Review, PTO-948	
☐ Notice of Informal Patent Application, PTO-152	
SEE OFFICE ACTION ON THE FOLLOWING PAGES	

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#### **DETAILED ACTION**

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1. Claims 1-18 are presented for examination.

### Claim Rejections - 35 U.S.C. § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 148 are rejected under 35 U.S.C. 102(b) as being unpatentable over patent # 6,018,340 ("Butler et al.")

As for claim 1, Butler et al. teach a method for supporting multiple displays per drawing surface (column 4, lines 24-25), the method comprises the steps of: a) receiving capability parameters regarding a first display of the multiple displays (column 17, lines 2-33); b) substituting selected display capabilities for the capability parameters (column 10, lines 38-53); and c) providing the selected display capabilities to an operating system (column 5, lines 19-29).

As for claim 2, Butler et al. teach a method of claim 1 further comprises determining the selected display capabilities based on a composite of the display parameters of each of the multiple displays (column 3, lines 31-37).

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As for claim 3, Butler et al. teach a method of claim 1 further comprises determining the selected display capabilities based on capabilities of a video graphics card (column 6, lines 28-39).

As for claim 4, Butler et al. teach a method of claim 1, wherein step (a) further comprises receiving the capability parameters in accordance with a system start-up (column 2, lines 24-37).

As for claim 5, Butler et al. teach a method of claim 4, wherein step (b) further comprises, in order,: identifying the capability parameters as primary parameters (column 9, lines 22-34) in accordance with a first portion of the system start-up (column 3, lines 1-10); providing the capability parameters to the operating system (column 1, lines 7-17) in accordance with the first portion of the system start-up (column 3, lines 1-10); and identifying the selected display capabilities (column 9, lines 14-33) as the primary parameters (column 13, lines 4-15) in accordance with a second portion of the system start-up (column 3, lines 1-10).

As for claim 6, Butler et al. teach a method of claim 1, wherein step (a) further comprises receiving the capability parameters in response to a monitor change process (column 10, lines 20-37).

As for claim 7, Butler et al. teach a multiple display Fig.3 supporting module (column 5, lines 19-28) comprises: a processing module (column 5, lines 55-63); and memory operably coupled to the processing module (column 1, lines 7-17), wherein the memory includes operational instructions that cause the processing module (column 5, lines 55-63) to (a) receive capability parameters regarding a first display of the multiple displays (column 17, lines 2-33);

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(b) substitute selected display capabilities for the capability parameters (column 10, lines 38-53); and © provide the selected display capabilities to an operating system (column 5, lines 19-29).

As for claim 8, Butler et al. teach a multiple display supporting module of claim 7, wherein the memory further comprises operational instructions that cause the processing module to determine the selected display capabilities based on a composite of the display parameters of each of the multiple displays (column 3, lines 31-37).

As for claim 9, Butler et al. teach a multiple display supporting module of claim 7, wherein the memory further comprises operational instructions that cause the processing module to determine the selected display capabilities based on capabilities of a video graphics card (column 5, lines 3-18).

As for claim 10, Butler et al. teach a multiple display supporting module of claim 7, wherein the memory further comprises operational instructions that cause the processing module to receive the capability parameters in accordance with a system start-up (column 2, lines 24-37).

As for claim 11, Butler et al. teach a multiple display supporting module of claim 10, wherein the memory further comprises operational instructions that cause the processing module to, in order,: identify the capability parameters as primary parameters (column 9, lines 22-34) in accordance with a first portion of the system start-up (column 3, lines 1-10); provide the capability parameters to the operating system (column 1, lines 7-17) in accordance with the first portion of the system start-up (column 3, lines 1-10); and identify the selected display

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capabilities (column 9, lines 14-33) as the primary parameters (column 13, 4-15) in accordance with a second portion of the system start-up (column 3, lines 1-10).

As for claim 12, Butler et al. teach a multiple display supporting module of claim 7, wherein the memory further comprises operational instructions that cause the processing module to receive the capability parameters in response to a monitor change process (column 10, lines 20-37).

As for claim 13, Butler et al. teach a digital storage medium for storing operational instructions that cause a processing module to support multiple displays associated with a drawing surface (column 3, lines 55-65), the digital storage medium comprises: first storage means for storing operational instructions that cause the processing module to receive capability parameters regarding a first display of the multiple displays (column 3, lines 1-11); second storage means for storing operational instructions that cause the processing module to substitute selected display capabilities for the capability parameters (column 14, lines 28-43); and third storage means for storing operational instructions that cause the processing module to provide the selected display capabilities to an operating system (column 1, lines 7-17).

As for claim 14, Butler et al. teach a digital storage medium of claim 13 further comprises means for storing operational instructions that cause the processing module to determine the selected display capabilities based on a composite of the display parameters of each of the multiple displays (column 3, lines 31-37).

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As for claim 15, Butler et al. teach a digital storage medium of claim 13 further comprises means for storing operational instructions that cause the processing module to determine the selected display capabilities based on capabilities of a video graphics card (column 6, lines 28-39).

As for claim 16, Butler et al. teach a digital storage medium of claim 13 further comprises means for storing operational instructions that cause the processing module to receive the capability parameters in accordance with a system start-up (column 2, lines 24-37).

As for claim 17, Butler et al. teach a digital storage medium of claim 16 further comprises means for storing operational instructions that cause the processing module to, in order,: identify the capability (column 9, 22-34) parameters as primary parameters (column 13, lines 4-15) in accordance with a first portion of the system start-up (column 3, lines 1-10); provide the capability parameters to the operating system (column 1, lines 7-17) in accordance with the first portion of the system start-up (column 3, lines 1-10); and identify the selected display capabilities (column 9, lines 14-33) as the primary parameters (column 13, lines 4-15) in accordance with a second portion of the system start-up (column 3, 1-10).

As for claim 18, Butler et al. teach a digital storage medium of claim 13 further comprises means for storing operational instructions that cause the processing module to receive the capability parameters in response to a monitor change process (column 10, lines 20-37).

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#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean Lesperance whose telephone number is (703) 308-6414. The examiner can normally be reached on from Monday to Friday between 8:00AM and 4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe, can be reached on (703) 305-4709. The fax phone number for the organization where this application or proceeding is assigned is (703) 308-6606.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 305-3900.

Jean Lesperance

Jean J-el

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Date 3-28-00

SUPERVISORY PATENT EXAMINER **GROUP 2700** 

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